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Application Number:	10774038			
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Title of Invention:	Thread selection unit and method to fairly allocate processor cycles in a block multithreaded processor			
First Named Inventor/Applicant Name:	Roger D. Arnold			
Customer Number:	38881			
Filer:	Laura C. Brutman/Brenda Bridges			
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Document Number	Document Description	File Name	File Size(Bytes) /Message Digest	Multi Part /.zip	Pages (if appl.)		
		j06580008ids.pdf	5986229	yes	153		
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	Document Description	Start	End		
	Information Disclosure Statement Letter	1	5		
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If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.

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